

FIG.1A

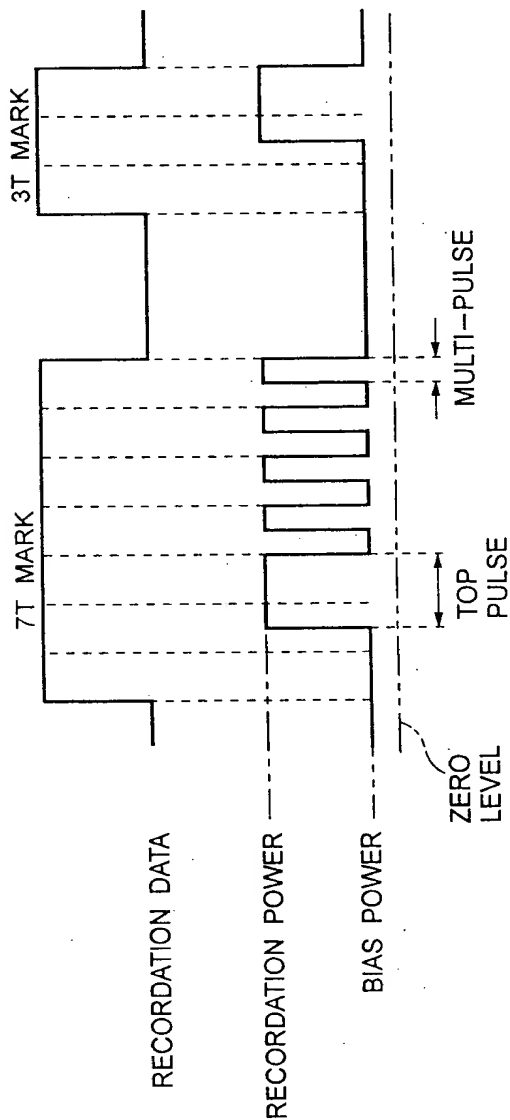


FIG.1B

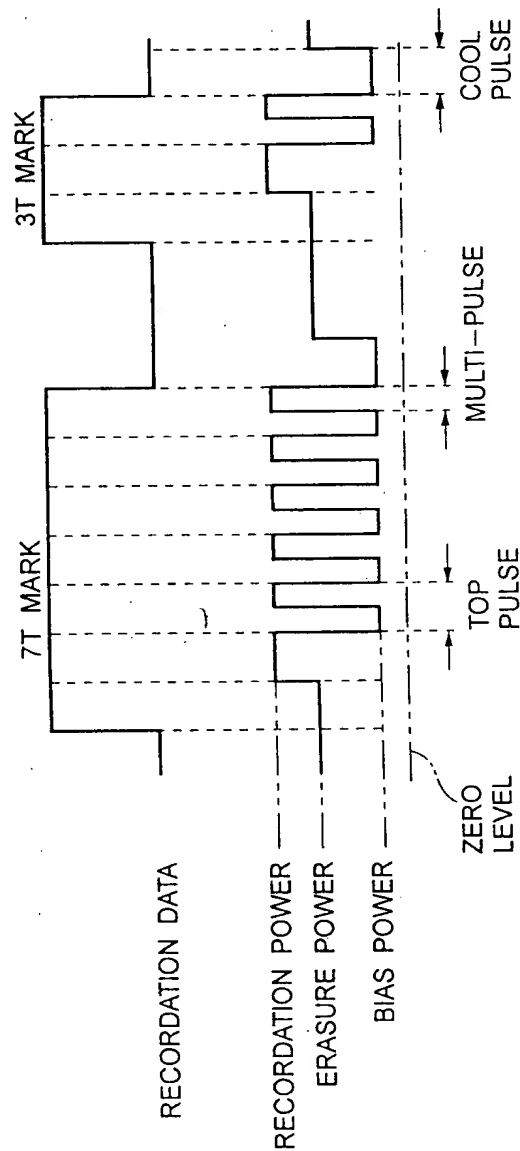


FIG.2A

FIG.2B

FIG.3A

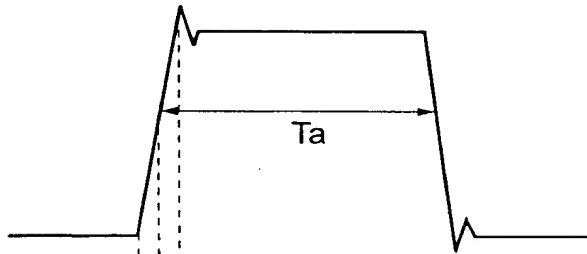


FIG.3B

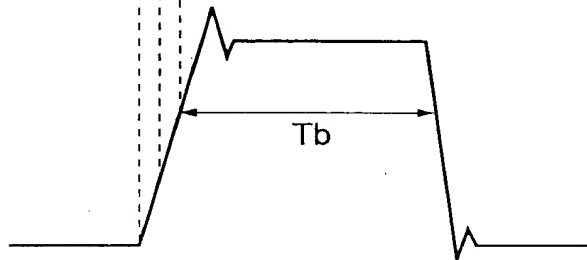


FIG.4A

WRITE PULSE

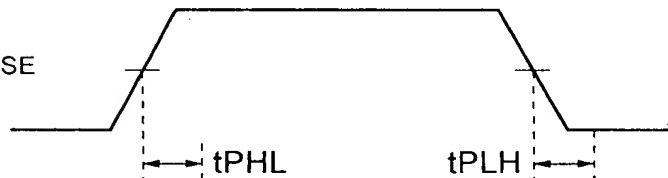


FIG.4B

IN PHASE OUTPUT

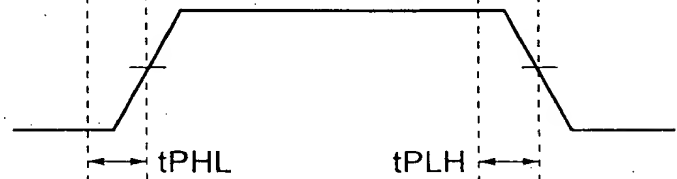
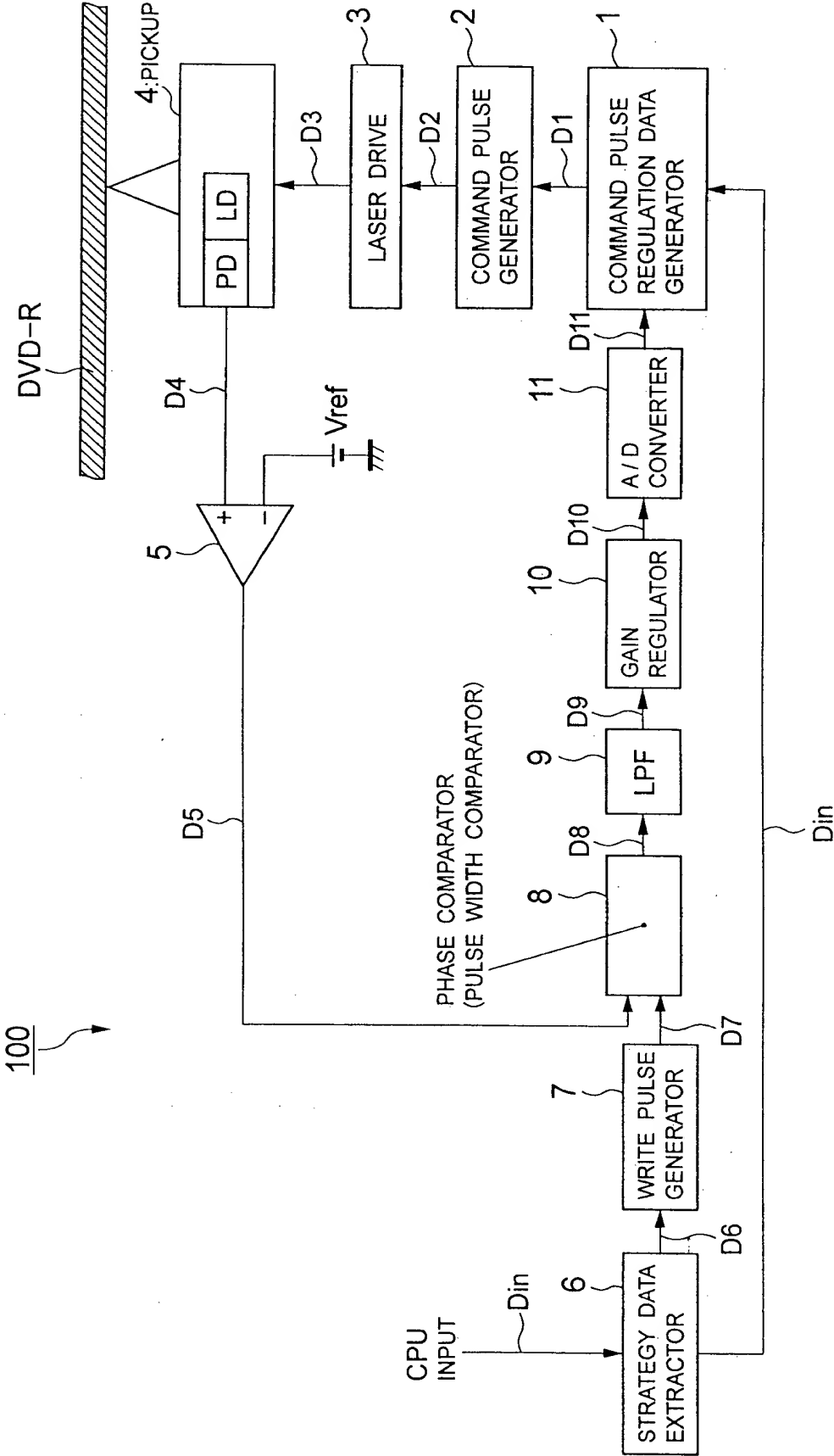


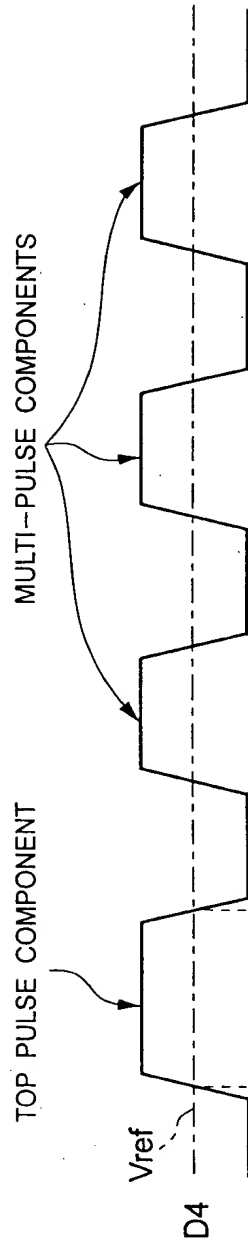
FIG.4C

INVERTED OUTPUT

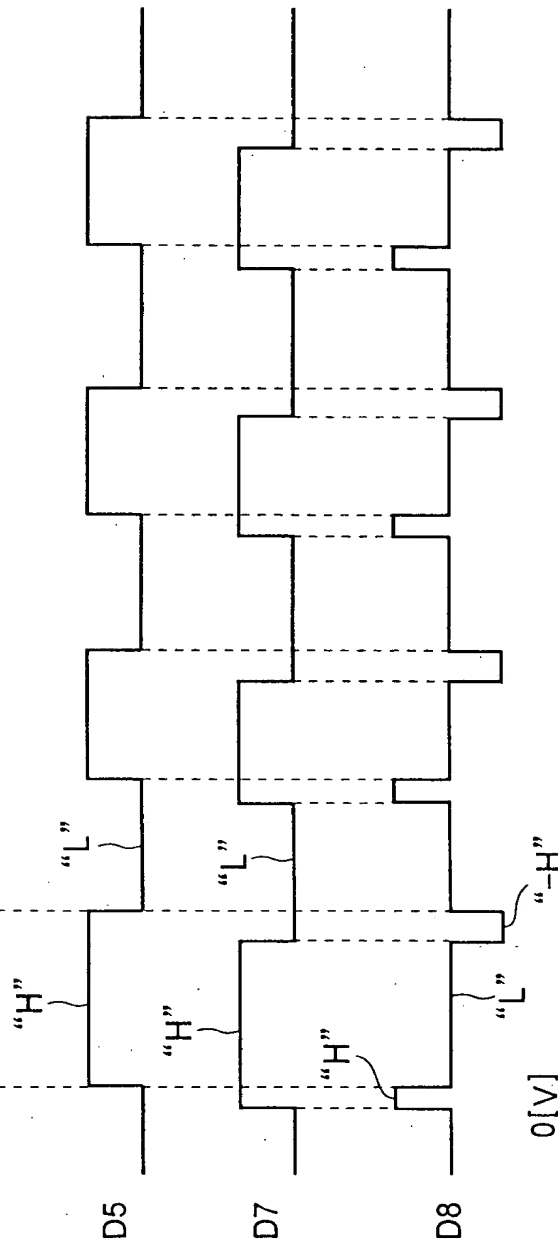


FIG.5





**FIG. 6A**



**FIG. 6B**

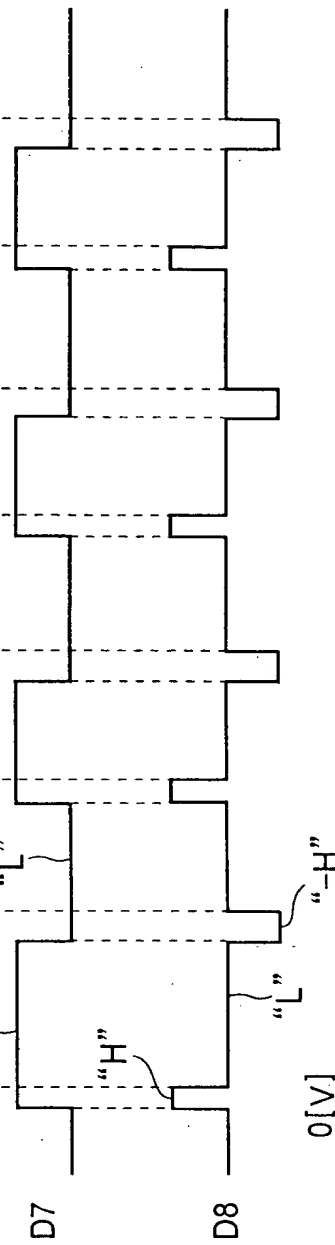


FIG. 6C

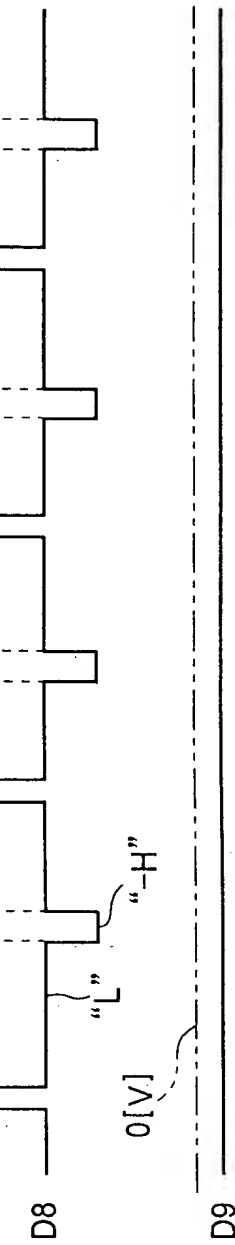


FIG. 6D

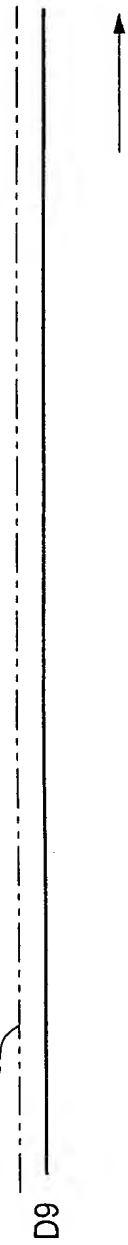


FIG. 6E

TIME 

5 / 11

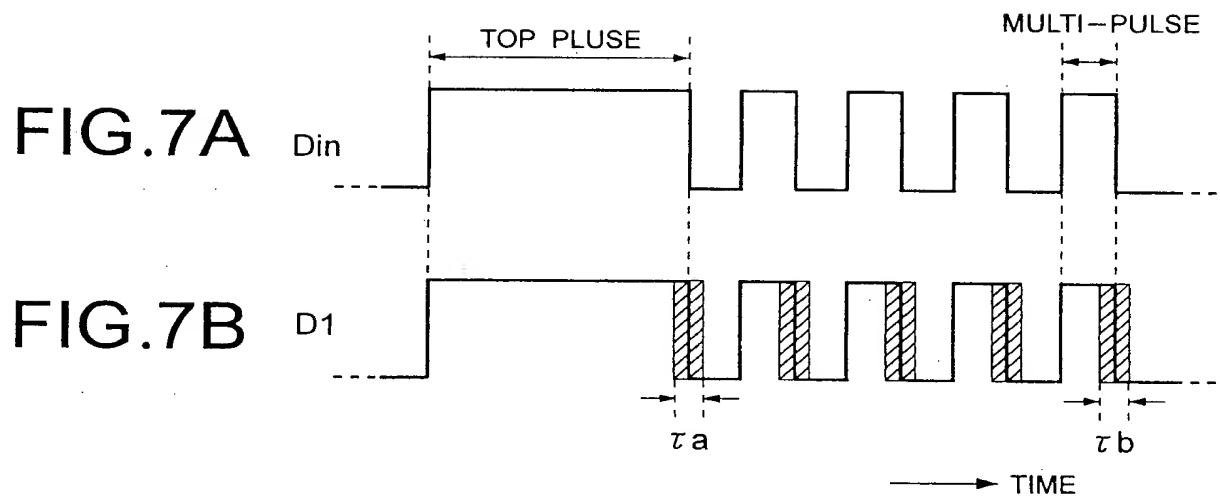
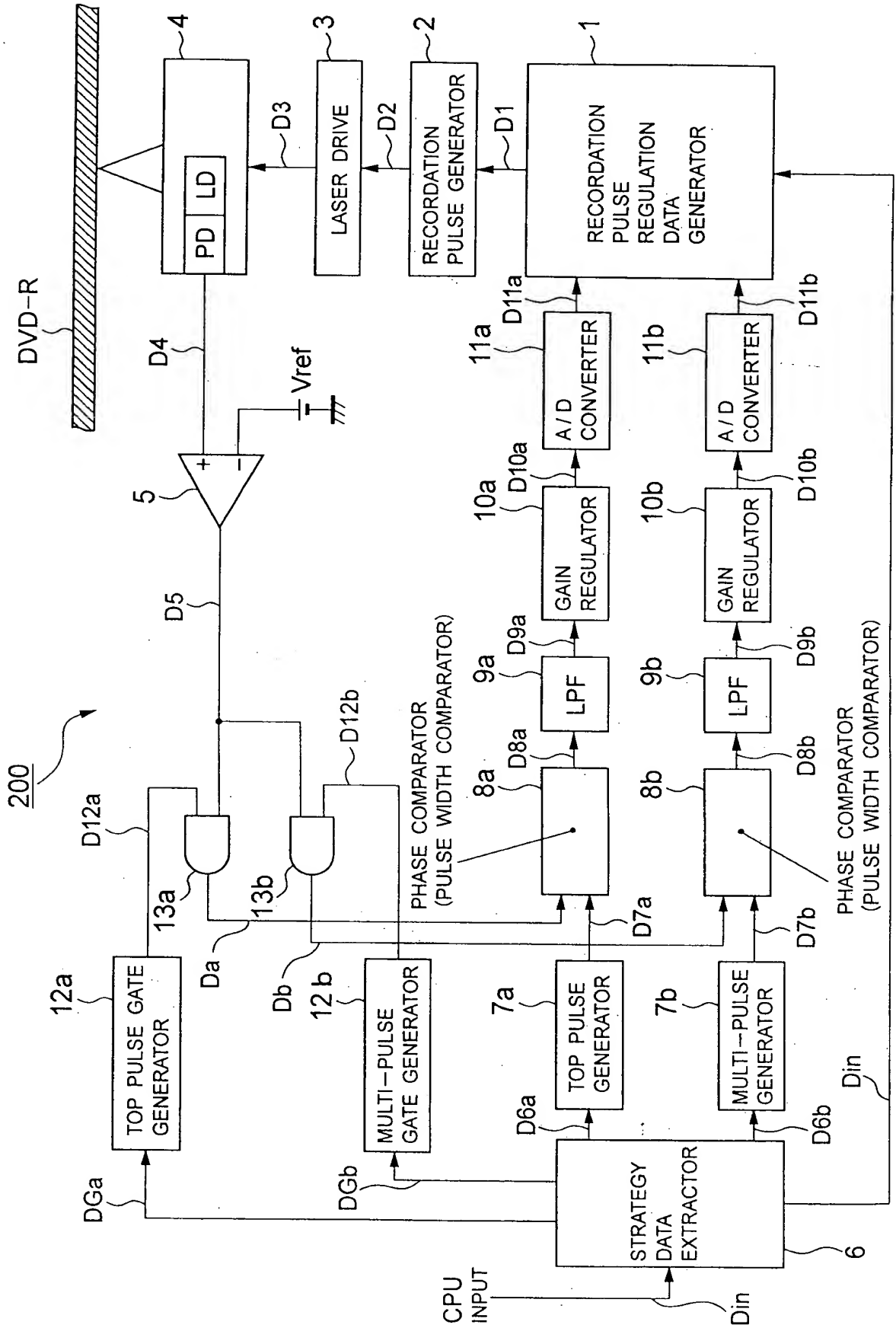


FIG. 8



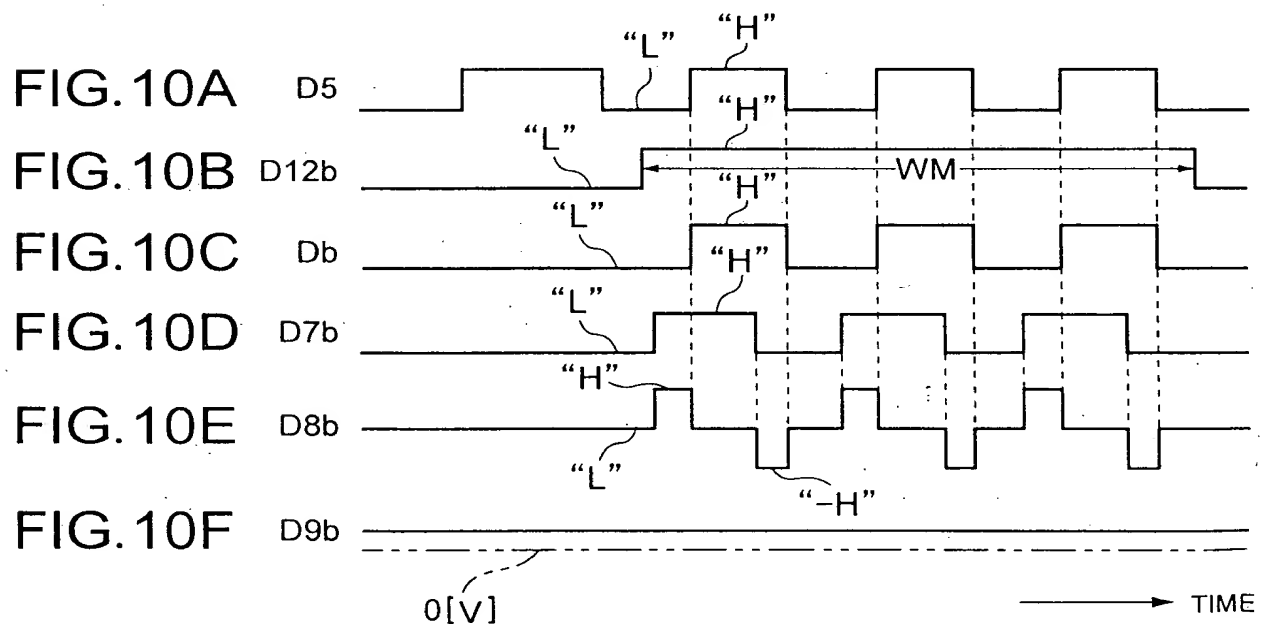
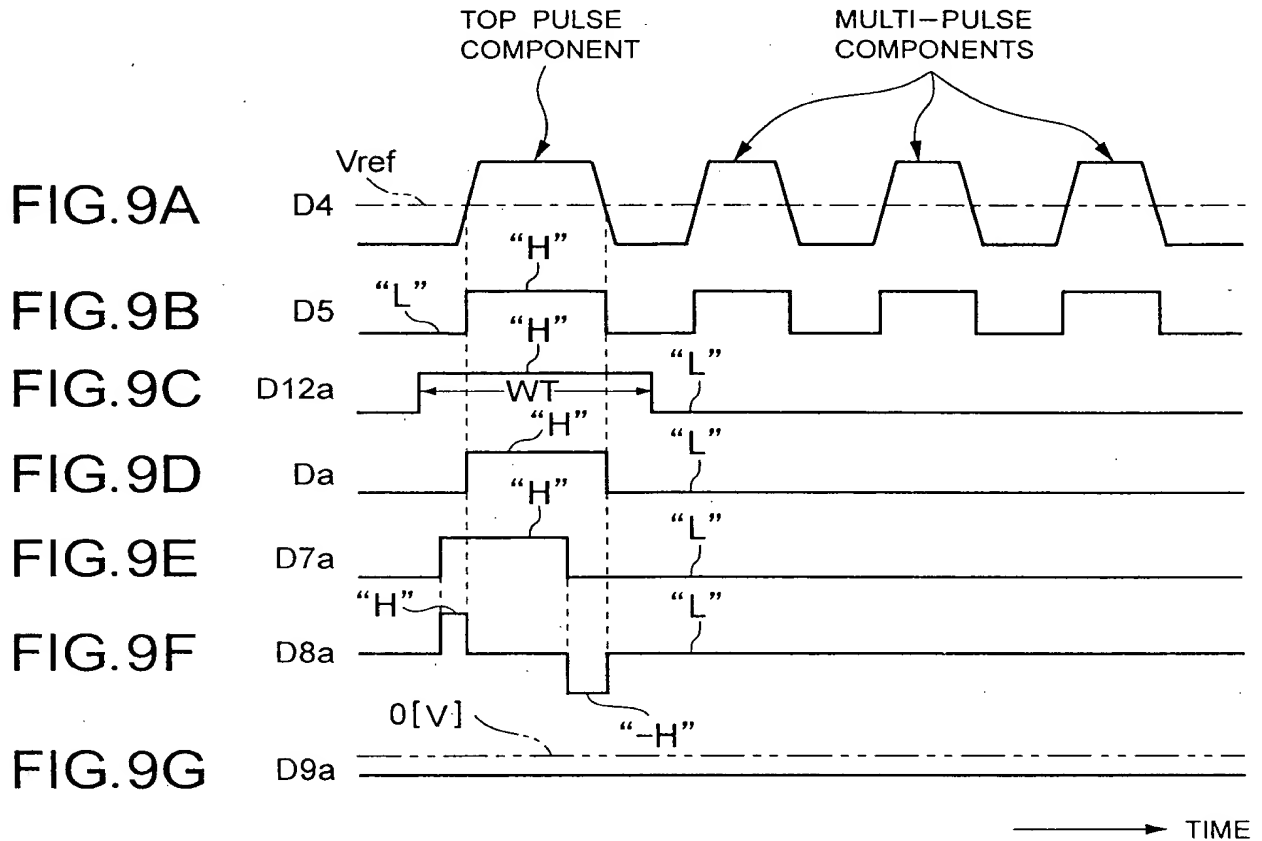
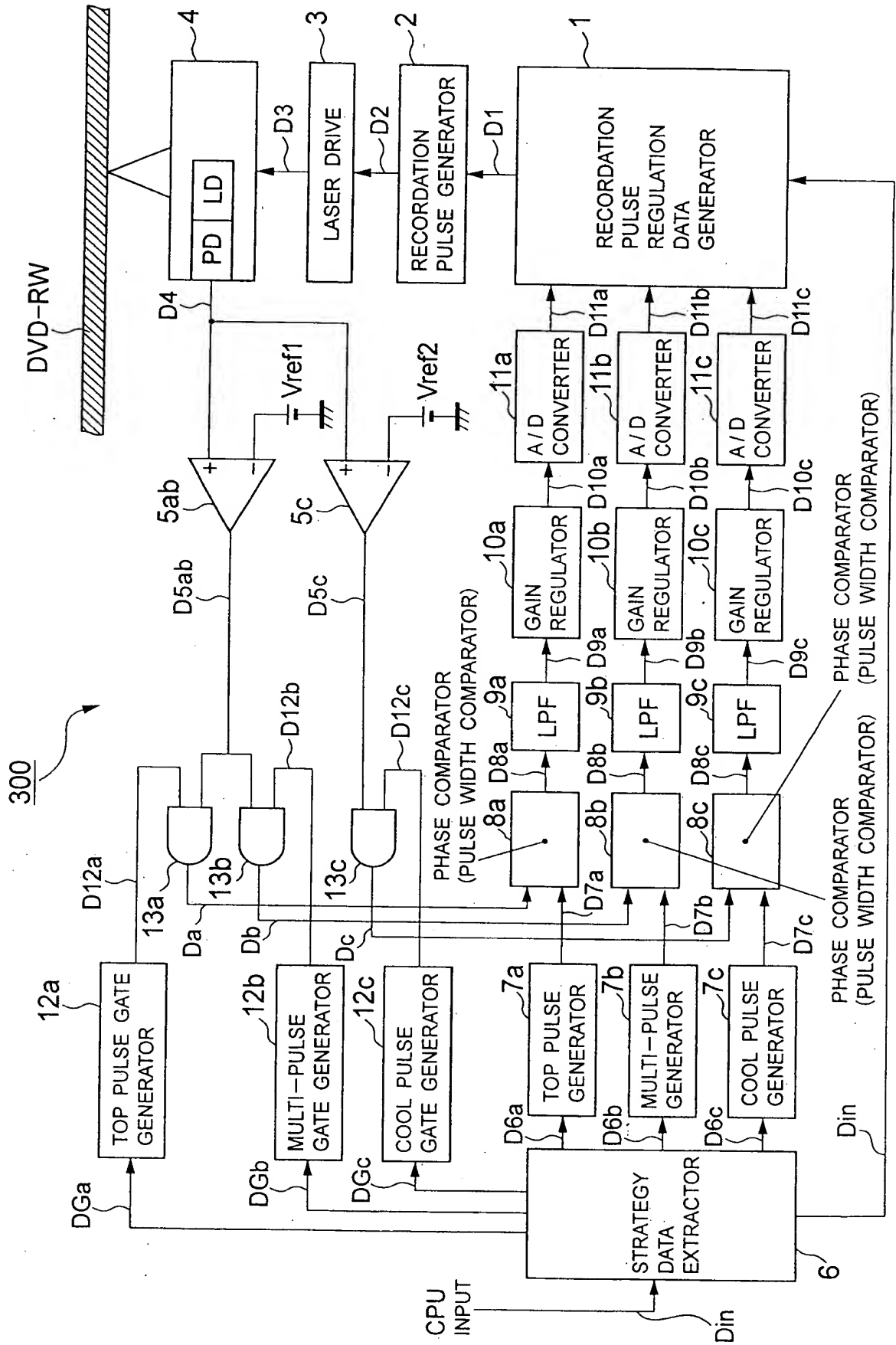


FIG. 11





9 / 11

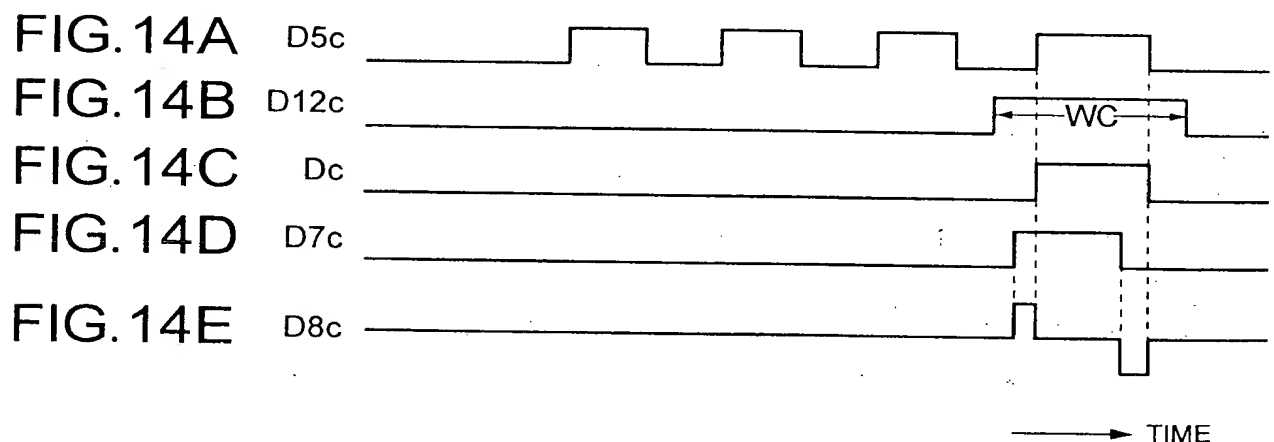
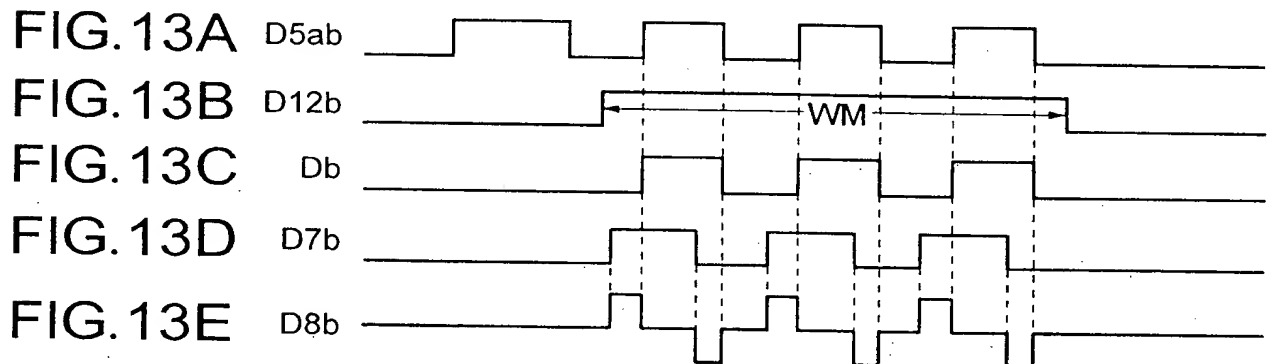
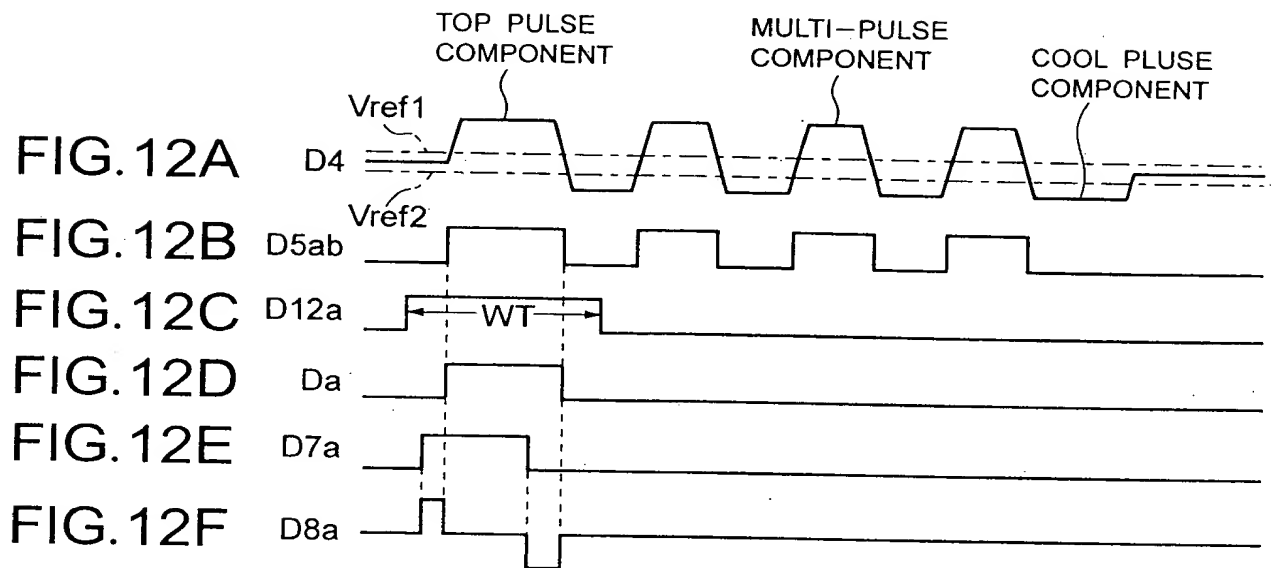


FIG.15A

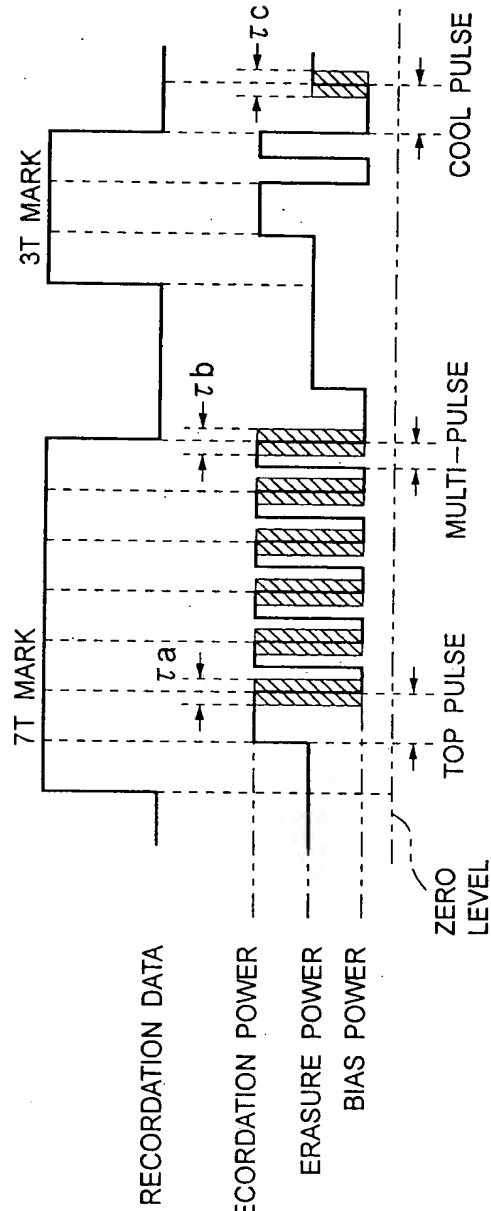


FIG.15B

FIG.16

